
Data Sheet

Type Description : **Low Offset Voltage Single Operational Amplifier**

Product Name : **EST.4158A**

Reversion : **V1.0**

Reversion Date : **May, 2016**

Page : **9 Pages**

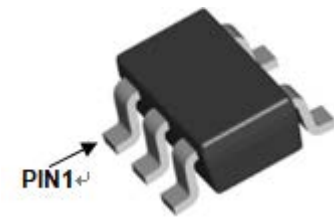
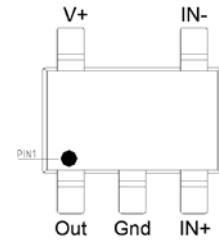
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GENERAL DESCRIPTION

The EST.4158A integrates a low power, low offset and high performance independent operational amplifier. It is also specifically designed to operate from a single power supply.

Now, it is available in a tiny SOT-23-5 package

PIN CONFIGURATION



SOT-23-5

APPLICATION

- Switching ac/dc adapter and battery charger
- General purpose controllers, instruments

KEY FEATURE

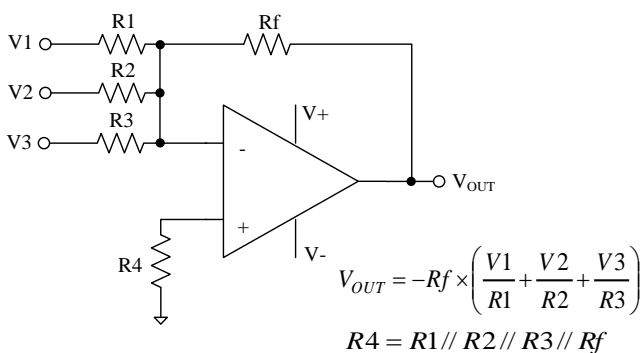
- ◆ Available in SOT-23-5 package, Halogen & Lead free
- ◆ Rail-to-rail input/output, Wide input common mode range : 0V ~ VCC+
- ◆ Supply range : 6V(+/-3V) to 26V(+/-13V)
- ◆ Supply current : 200uA (@ VCC+ =15V)
- ◆ Low offset voltage : ±250uV
- ◆ Low offset voltage drift : 7uV/°C
- ◆ Unity gain bandwidth : 1MHz
- ◆ Common mode rejection : 80dB
- ◆ Hazardous Substance Free
- ◆ RoHs/REACH Compliant

ORDERING INFORMATION

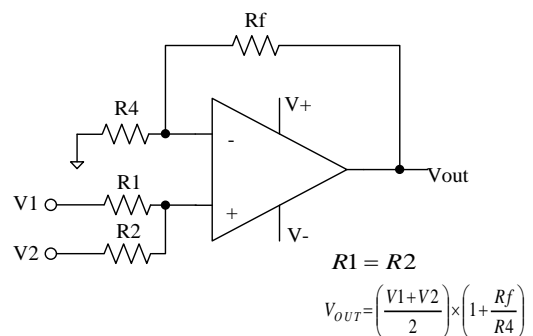
Part Number	Package	Packaging	Note
EST.4158A	SOT-23-5	Tape & Reel	Green

Note: EST lead-free products contain molding compounds/die attach materials and 100% matte tin plate termination finish; which are fully compliant with RoHS .EST lead-free products meet or exceed the lead-free requirements of IPC/JEDEC J-STD-020C for MSL classification at lead-free peak reflow temperature. EST defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

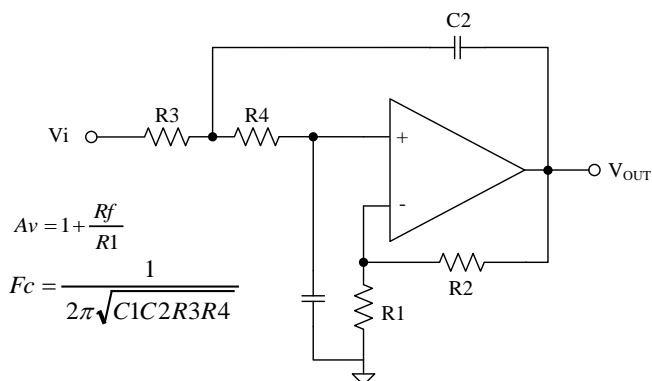
APPLICATION CIRCUIT



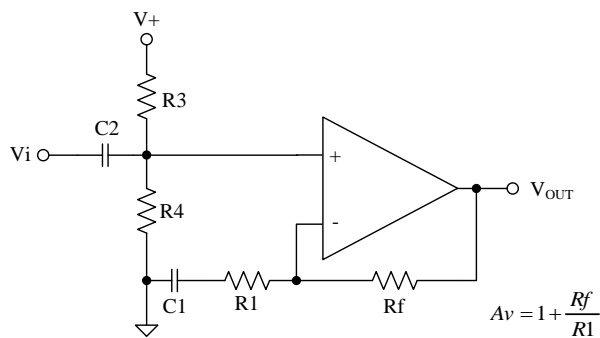
Inverting Summing Amplifier Circuit



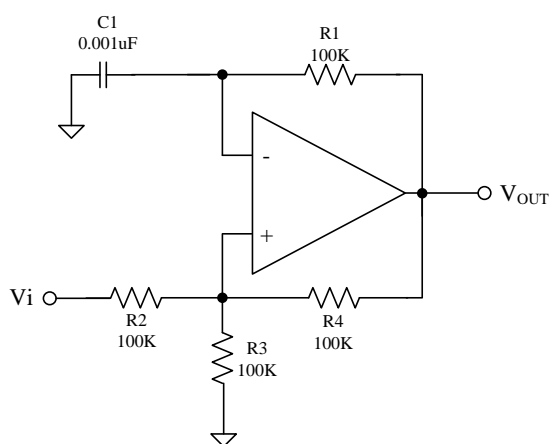
Non-Inverting Summing Amplifier Circuit



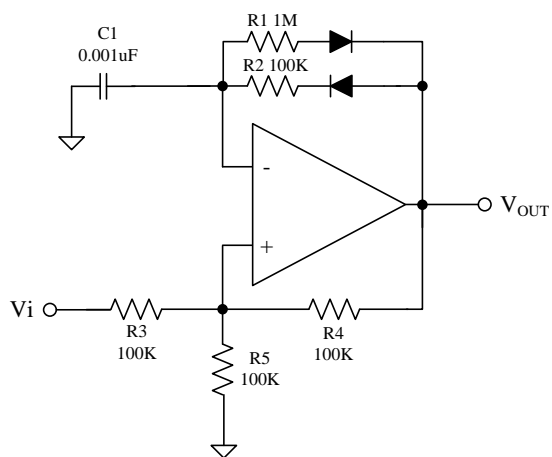
Second Order Active Low-Pass Filter



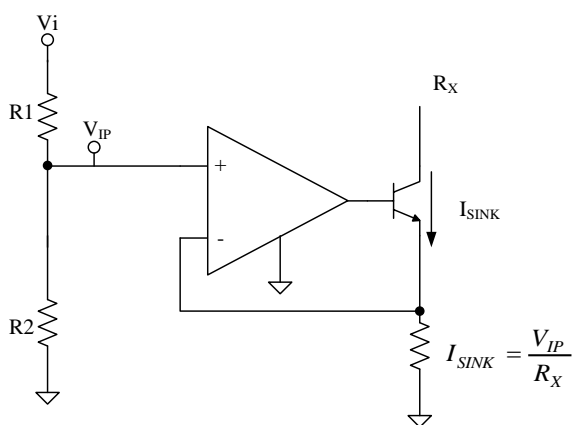
AC Coupled Non-inverting Amplifier



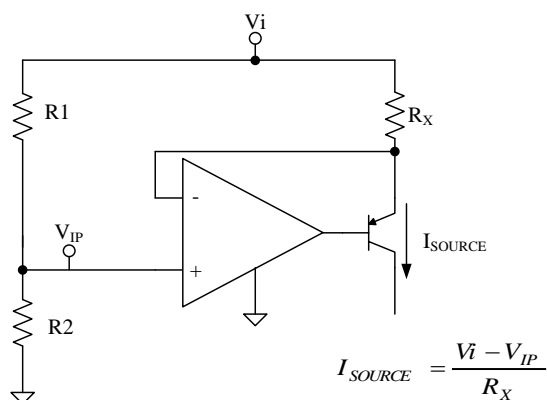
Squarwave Oscillator



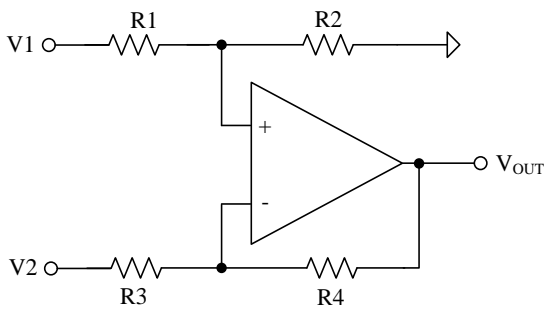
Pulse Generator



Current Sink



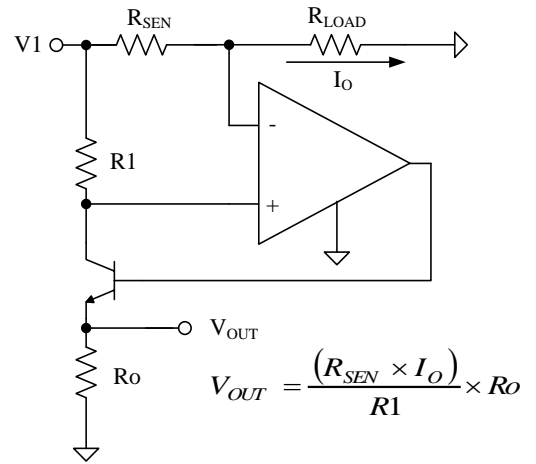
Current Source



$$R1 = R3, R2 = R4$$

$$V_{OUT} = (V1 - V2) \times \frac{R4}{R3}$$

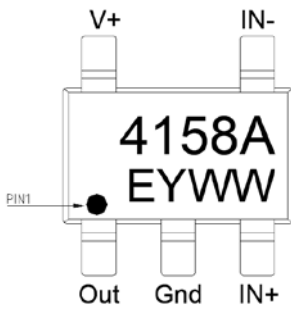
Differential Amplifier



$$V_{OUT} = \frac{(R_{SEN} \times I_O)}{R1} \times R_o$$

Current Monitor Amplifier

PIN DESCRIPTION



E: EST
Y: Year
WW: Weeks

SOT-23-5	NAME Description	Description
1	Output	Output of OP AMP.
2	GND	GND
3	IN+	OP Non-inverting Input
4	IN-	OP Inverting Input
5	V+	Positive power supply

Absolute Maximum Ratings

Parameter Symbol	Symbol	Limit Values		Unit	Remark
		Min.	Max		
Supply Voltage Vcc	V _{CC}	-0.3	26	V	
Differential Input Voltage	V _{ID}		26	V	
Input Voltage	V _i	-0.6	26	V	
OP Output Voltage	V _o	-0.3	V _{CC}	V	
Operation Junction Temperature	T _j	-40	150	°C	
Storage Temperature	T _{stg}	-55	150	°C	
Package Thermal Resistance	SOT-23-5	θ _{JA}	-	245	°C/W
Junction to case Thermal Resistance		θ _{JC}		55	°C/W
Power Dissipation @TA=25°C	SOT-23-5	PD	-	0.4	W
Lead temperature (Soldering, 10 sec)			-	260	°C
ESD Voltage Protection	HBM	VESD-HBM	-	2.0	KV
	MM	VESD-MM	-	200	V

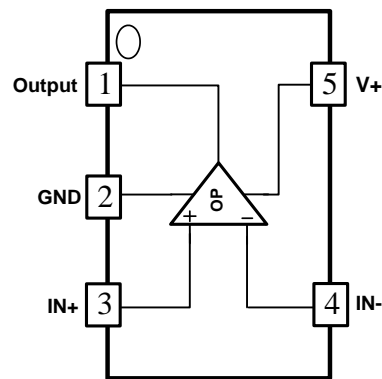
Stress beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

*Free standing with no heatsink; without copper clad.(Measurement condition – just before junction temperature T_J enters into OTP)

**Measure on the PKG top surface

DC Electrical Characteristics (V_{CC}⁺=15V, V_{CC}⁻=0V Ta=25°C)

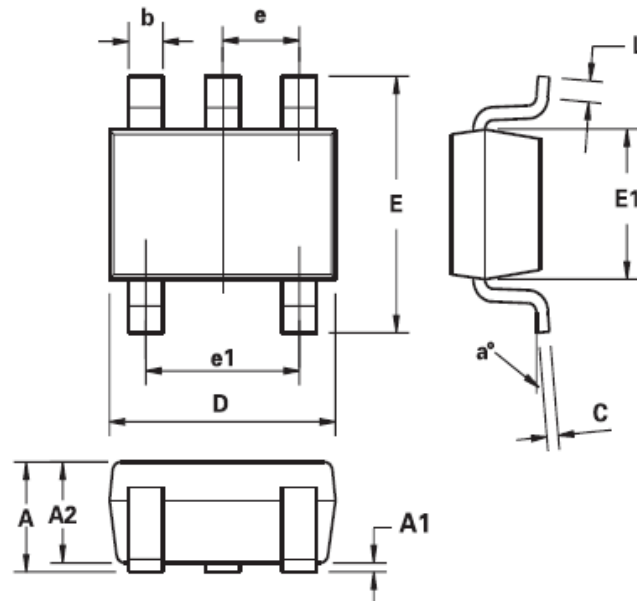
Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Positive Supply Voltage	V _{CC} ⁺	6		25	V	
Total Supply Current	I _{CC}			200	μA	1. V _{CC} ⁺ = 15V 2. no load,
Input Offset Voltage	V _{IO}	-250	0	250	μV	25°C
		-350	0	350		-25~85°C
Small Signal Gain	A _{vd}		80		dB	
Common Mode Rejection Ratio	CMRR			80	dB	
Power Supply Rejection Ratio	PSRR			80	dB	
Input Common Mode Voltage Range	V _{icm}	0		V _{CC}	V	V _{CM} =0V to V _{CC} . R _S ≤10KΩ
Output Current Source	I _{source}			10	mA	1. V _{ID} = +1V 2. V _{CC} ⁺ = 15V 3. V _O = 2V
Output Current Sink	I _{sink}			15	mA	1. V _{ID} = -1V 2. V _{CC} ⁺ = 15V 3. V _O = 2V
Output Short Circuit Current to Ground	I _{sc}			10	mA	V _{CC} ⁺ = 15V
High Level Output Voltage	V _{OH}			V _{CC}	V	1. R _L = 10K 2. V _{CC} ⁺ = 15V
Low Level Output Voltage	V _{OL}		20		mV	R _L = 10K
Slew Rate at Unity Gain	SR		0.5		V/us	1. V _i = 0.5 to 3V 2. V _{CC} ⁺ = 15V 3. R _L = 2K 4. C _L = 100pF 5. unity gain connection
Gain Bandwidth Product	GBP		1		MHz	1. f = 100KHz 2. V _{CC} ⁺ =15V 3. R _L = 2K 4. C _L = 100pF

BLOCK DIAGRAM**CAUTION**

This integrated circuit has been designed carefully in the ESD protection ability. Failure to observe proper handling and installation procedures may cause damage. Recommend that all integrated circuits should be handled with appropriate precautions.

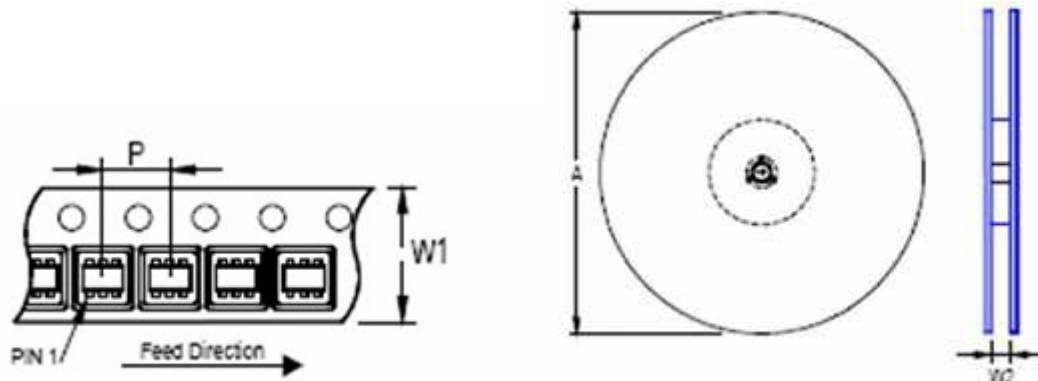
PACKAGE OUTLINES

SOT23-5



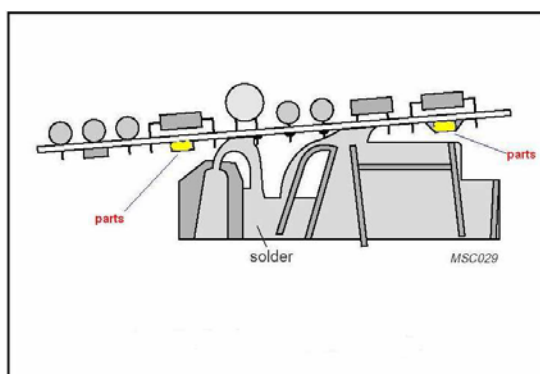
DIM	Millimeters	
	Min.	Max.
A	0.90	1.45
A1	0.00	0.15
A2	0.90	1.30
b	0.20	0.50
C	0.09	0.26
D	2.70	3.10
E	2.20	3.20
E1	1.30	1.80
e	0.95 REF	
e1	1.90 REF	
L	0.10	0.60
a°	0°	30°

TAPE REEL DATA

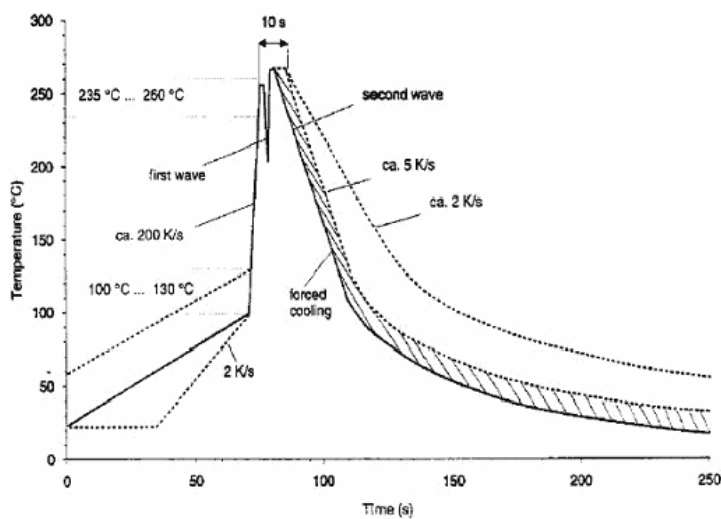


Package Type SOT-26	Tape Size (W1) (mm)	Pocket Pitch (P) (mm)	Reel Size (A) (mm)	Reel Width (W2) Min./Max. (mm)	Units Per Reel pcs.
6 Lead	8	4	180	8.4/9.9	3000

WAVE SOLDERING PROCESS



WAVE SOLDERING PROFILE



Double-Wave-Soldering, Temperature/Time - Profile
(Lead-Temperature)
Full line: typical process / Dotted line: process limits



Update History

Revision	Date	Update
1.0	May 06, 2018	Preliminary version

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